

Docket No. 212812US-2 S

IN RE APPLICATION OF: Takashi IPPOSHI et al.

SERIAL NO: 09/930,202

FILED: AUGUST 16, 2001

FOR: SEMICONDUCTOR WAFER



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ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Transmitted herewith is an amendment in the above-identified application.

- ☒ No additional fee is required
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- ☒ Additional documents filed herewith: MARKED-UP COPY
LETTER REQUESTING APPROVAL OF DRAWING CHANGES

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	10	MINUS	20	0	× \$18 =	\$0.00
INDEPENDENT	3	MINUS	3	0	× \$84 =	\$0.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+ \$280 =	\$0.00
		TOTAL OF ABOVE CALCULATIONS				\$0.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$0.00
		<input type="checkbox"/> Recordation of Assignment			+ \$40 =	\$0.00
		TOTAL				\$0.00

- ☐ A check in the amount of _____ is attached.
- ☒ Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- ☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

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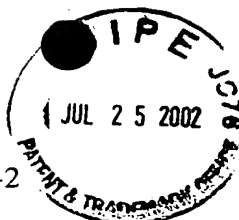
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212812US-2524-57-2



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

TAKASHI IPPOSHI ETAL.

SERIAL NO: 09/930,202

FILED: AUGUST 16, 2001

FOR: SEMICONDUCTOR WAFER

:

: GROUP ART UNIT: 2815

: EXAMINER: JOSEPH NGUYEN

:

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AMENDMENT AND REQUEST FOR RECONSIDERATION

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

In response to the official action dated April 26, 2002, please amend the above
identified patent application as follows:

IN THE CLAIMS

Please amend claims 1 and 8 as follows.

*subcl
a1*

1. (Amended) A semiconductor wafer comprising:
first and second semiconductor wafers having crystal orientation display sections to be
nicks indicative of crystal orientations formed on fringes thereof,
wherein said crystal orientation display sections are indicative of an identical crystal
orientation in said first and second semiconductor wafers, and
said first and second semiconductor wafers are bonded with said crystal orientation
display sections shifted from each other.

a2 *SUB
B2*

8. (Amended) A semiconductor wafer comprising:
first and second semiconductor wafers having bulk structures,